

Tutorial Conclusion

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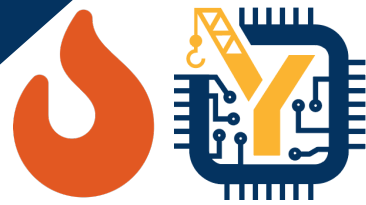
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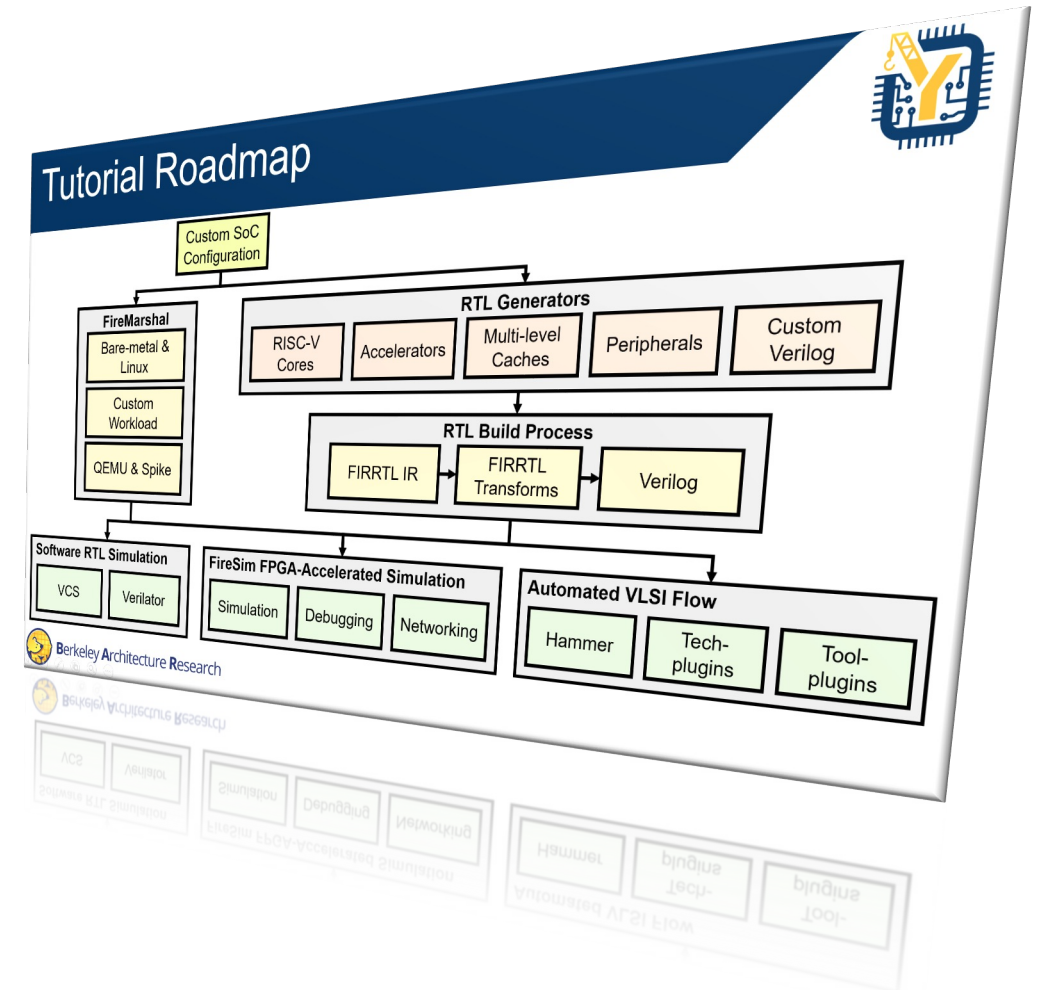
Berkeley
Architecture
Research



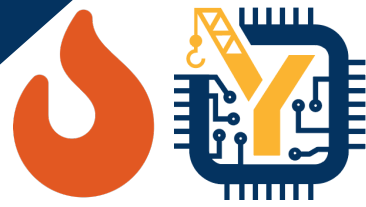
Recap



- Chipyard Basics
 - Composing SoC using generators
 - **New!:** Constellation NoC generator
 - Adding custom accelerators
 - Simulation
 - VLSI flow:
 - **New!:** fully open-source RTL to GDS flow
- FireSim
 - Full-system FPGA-accelerated simulation
 - Linux-based software workloads
 - Debugging and instrumentation
 - Network simulation
 - **New!:** distributed metasim support!
 - **New!:** local (on-premises) FPGA support!



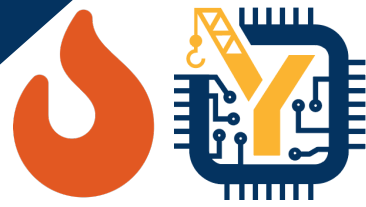
Join The Community!



- Used in industry and academia
- Development is all open-source and on GitHub
 - “main” branch is active development, may be unstable
 - We recommend using tagged releases (e.g. Chipyard 1.8.0 / FireSim 1.15.0 released yesterday 😊)
- Sub-projects managed using submodules
- Hundreds of pages of documentation!
 - If something isn't clear, please let us know
- We appreciate feedback! We appreciate PRs even more!
- Thank you for attending!



Learn More



- Chipyard

- Github: <https://github.com/ucb-bar/chipyard/>
- Docs: <https://chipyard.readthedocs.io/en/latest/index.html>
- Mailing List: <https://groups.google.com/forum/#!forum/chipyard>



- FireSim

- Website: <https://fires.im/>
- Github: <https://github.com/firesim/firesim/>
- Docs: <https://docs.fires.im/en/latest/>
- Mailing List: <https://groups.google.com/forum/#!forum/firesim>



Tutorial Feedback:

<https://fires.im/tutorial-feedback/>

