Tutorial Conclusion

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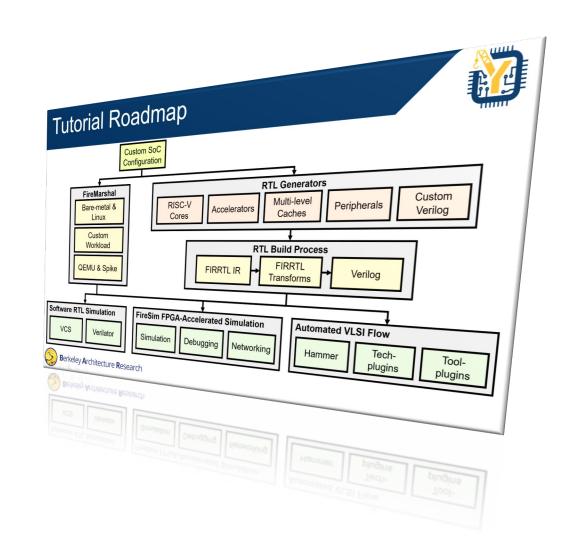




Recap



- Chipyard Basics
 - Composing SoC using generators
 - Adding custom accelerators
 - Simulation
 - VLSI flow
- FireSim
 - Full-system FPGA-accelerated simulation
 - Linux-based software workloads
 - Debugging and instrumentation
 - Network simulation



Join The Community!



- Used in industry and academia
- Development is all open-source and on Github
 - Stable `master` branch (latest release)
 - Less-stable `dev` branch with all the newest features
- Sub-projects managed using submodules
- Over 100 pages of documentation!
 - If something isn't clear, please let us know
- We appreciate feedback! We appreciate PRs even more!
- Thank you for attending! This was our first attempt at a full day tutorial
 - We have stickers! Please take some



Learn More



- Chipyard
 - Github: https://github.com/ucb-bar/chipyard/
 - Docs: https://chipyard.readthedocs.io/en/latest/index.html
 - Mailing List: https://groups.google.com/forum/#!forum/chipyard
- FireSim
 - Website: https://fires.im/
 - Github: https://github.com/firesim/firesim/
 - Docs: https://docs.fires.im/en/latest/
 - Mailing List: https://groups.google.com/forum/#!forum/firesim



CHIPYARD



https://fires.im/tutorial-feedback/

