



On-Premise FPGA Demo

<https://firesim.com>



@firesimproject

**HPCA Tutorial 2023**

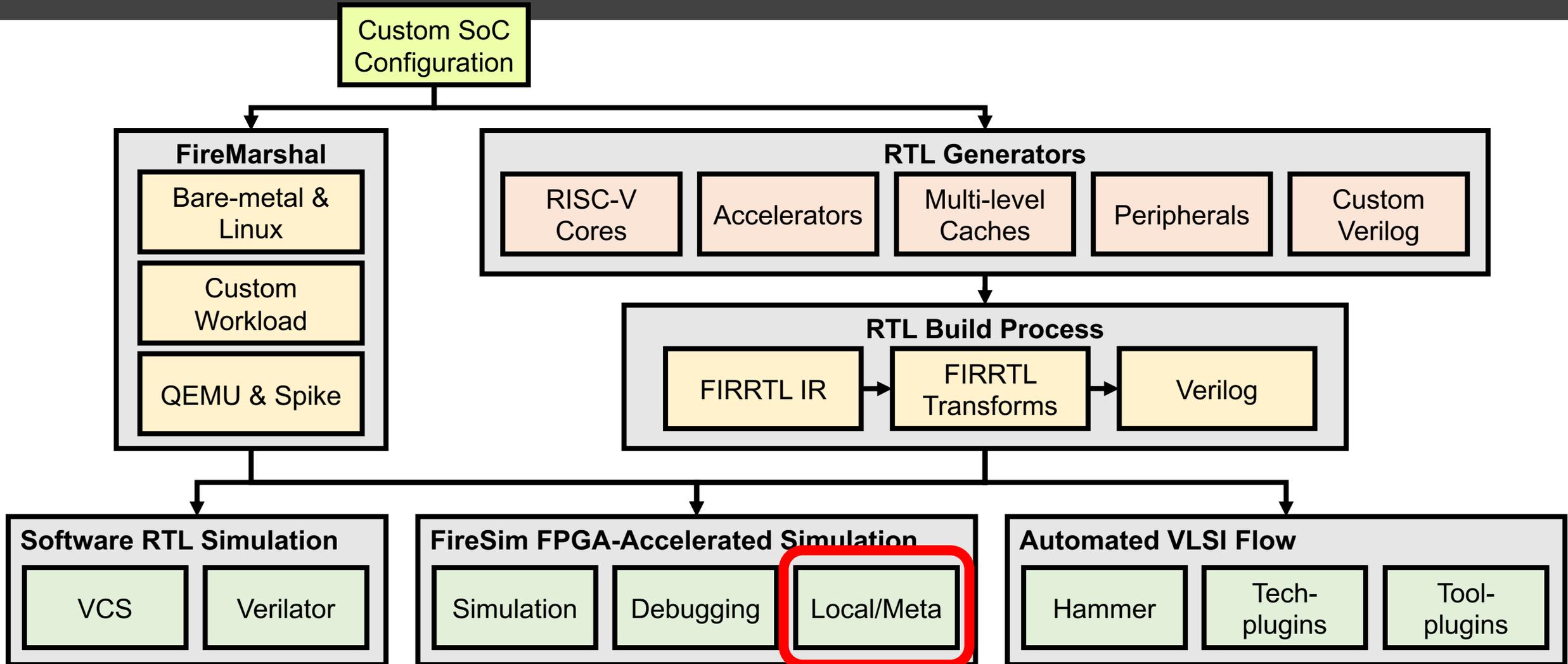
Abraham Gonzalez



**Berkeley Architecture Research**



# Tutorial Roadmap





# Two new FireSim features!

- Local FPGA support!
  - Re-architecting of FireSim Manager, Drivers, and Shells to support adding new FPGA and host platforms
    - Now supports Xilinx Alveo XRT-enabled FPGAs (e.g. U250)
- Distributed Meta-simulations
  - Early-stage accelerator development requires running many parallel verilator/vcs sims, but these are traditionally accessed via Make-system in Chipyard/FireSim
  - FireSim manager now supports distributing metasims using the same machinery as distributing FPGA simulations, on both EC2 and local machines
    - Same user-interface for workload/job specification/mapping, constructing heterogeneous systems, running sims, and collecting outputs (now including waveforms)



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# FireTower v1 Machine Specs

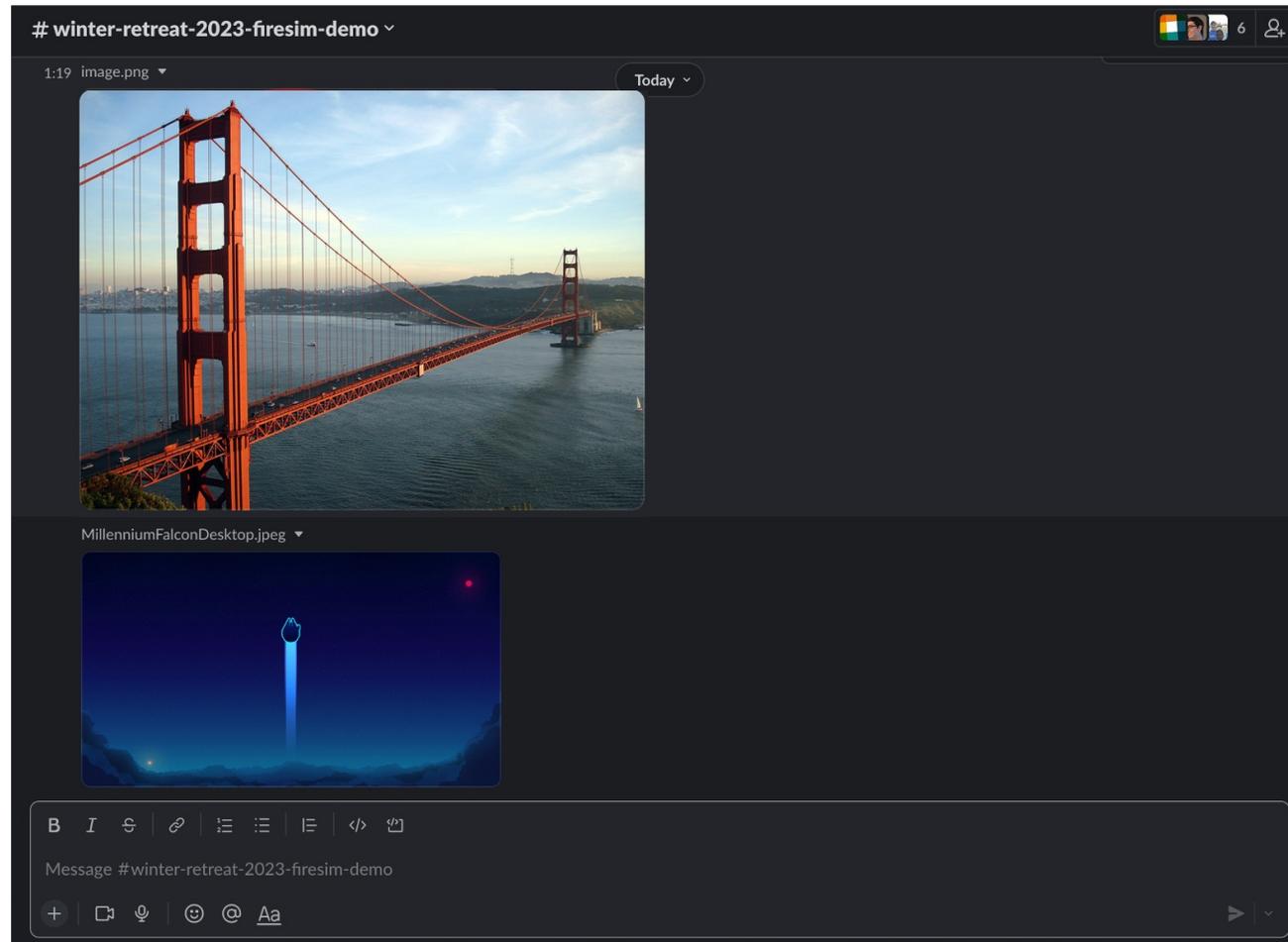
- Intel Core i7 13700K
  - Liquid cooler (w/RGB)
- 32 GB DDR4 (w/RGB)
- **Xilinx Alveo U250 (active)**
- Motherboard spec'd for:
  - 2 U250 + GPU
  - OR
  - 3 U250
- 1500W PSU to support multi-FPGA/GPU
- Thermaltake Core P3 Red Case
- Ubuntu 18.04

\$1500 without FPGAs or GPUs



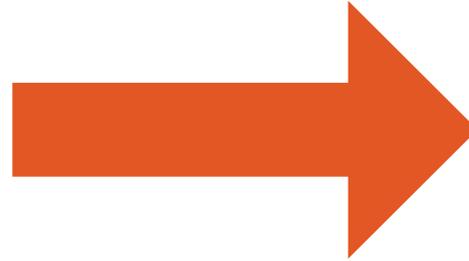


# Demo Lifetime



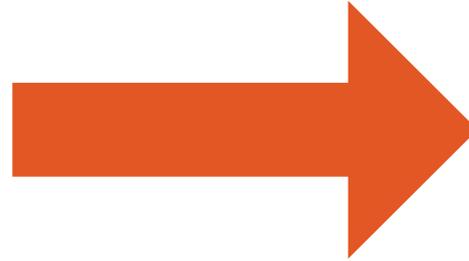


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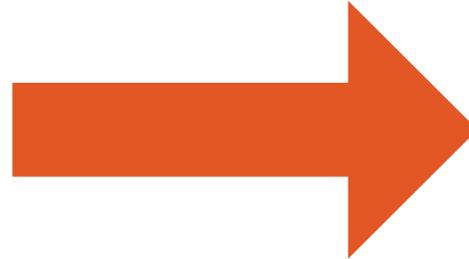


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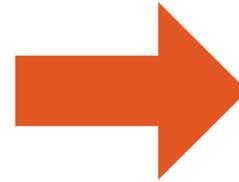


# Demo Lifetime





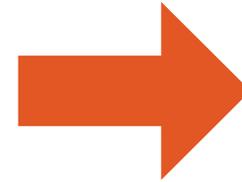
# Demo Lifetime



Convert to .png



# Demo Lifetime



Convert to .png

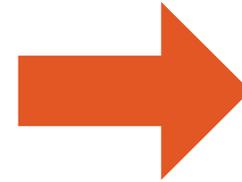


 PyTorch

Image  
Preprocessing



# Demo Lifetime

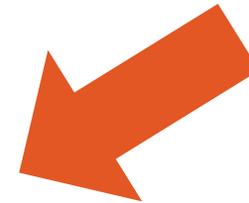


Convert to .png



 PyTorch

Image  
Preprocessing



Compile target  
Gemmini C binary  
with image



# Demo Lifetime



 FireSim

Run FireSim  
Rocket + Gemmini  
simulation



Compile target  
Gemmini C binary  
with image



# Demo Lifetime



 FireSim

Run FireSim  
Rocket + Gemmini  
simulation



Compile target  
Gemmini C binary  
with image

Reset + Flash FPGA

Copying collateral

Running simulation



# Demo Lifetime



 FireSim

Run FireSim  
Rocket + Gemmini  
simulation



“It’s a **bridge**”



Compile target  
Gemmini C binary  
with image



# Demo Lifetime



 FireSim

Run FireSim  
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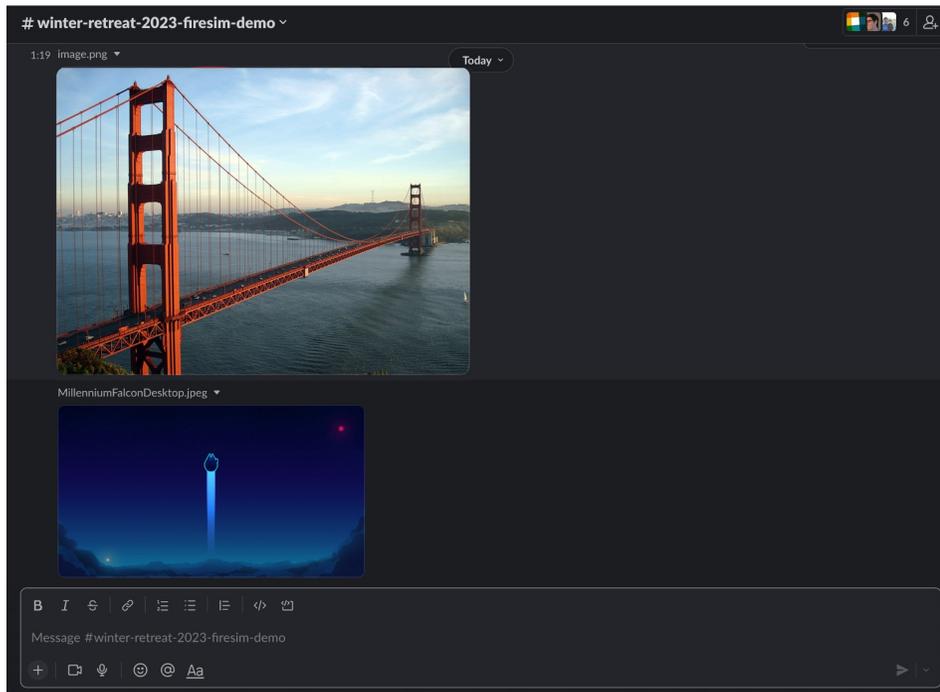
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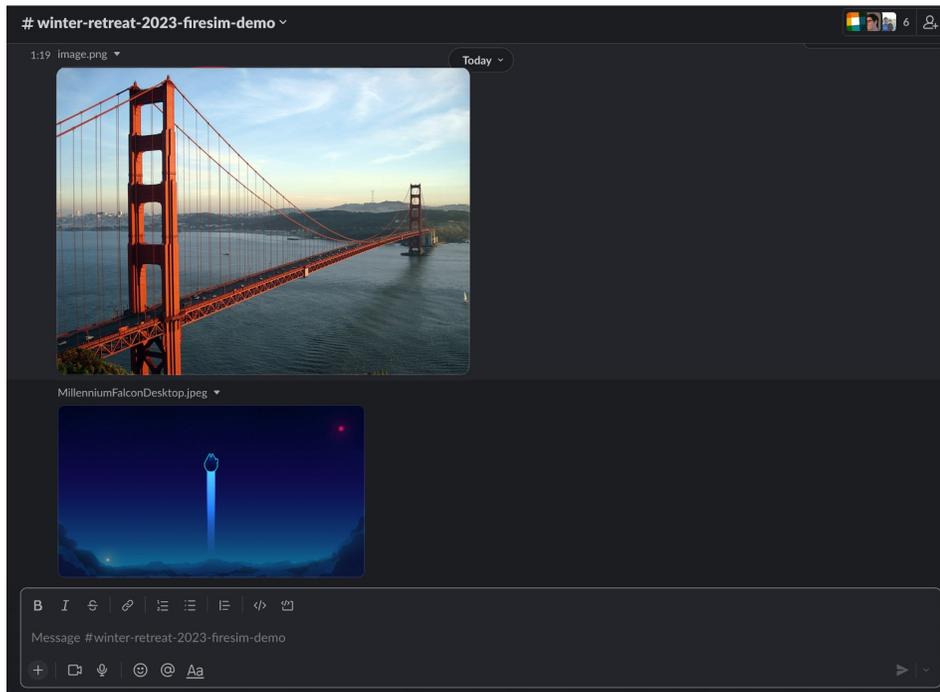
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"It's a bridge"



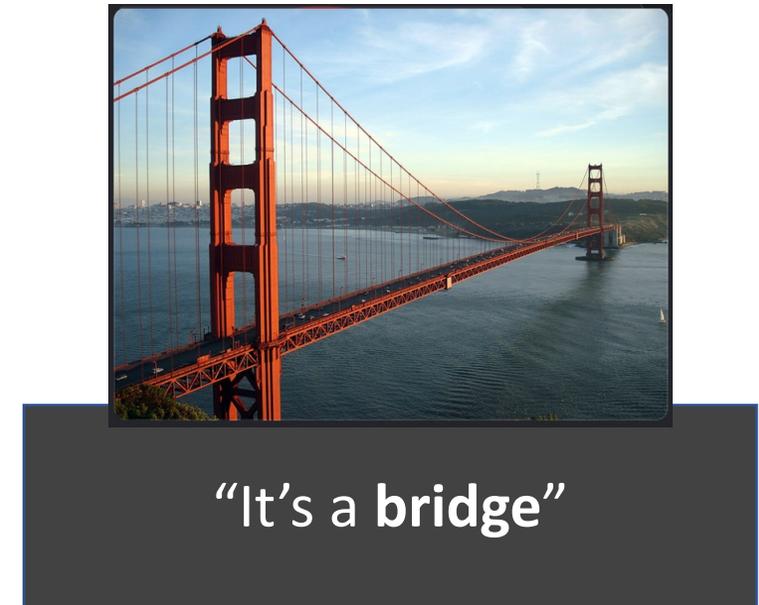
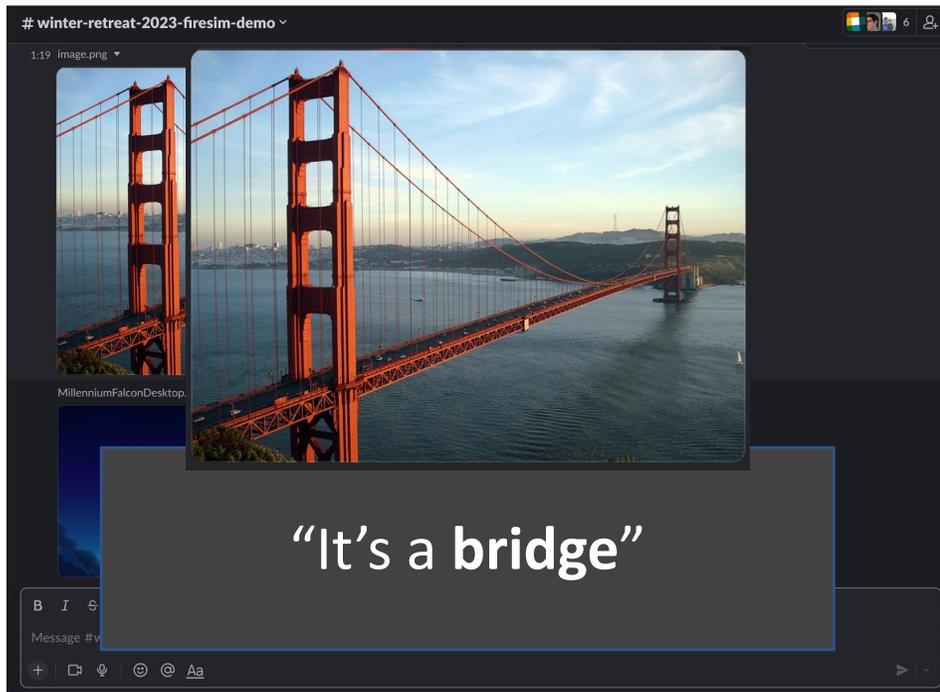
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"It's a bridge"

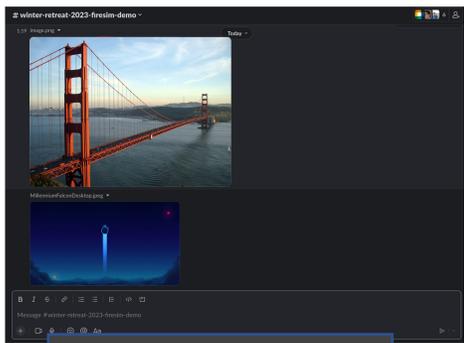


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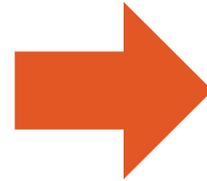
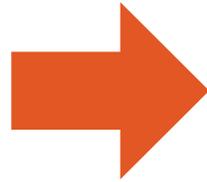




# Let's Run the Demo!



Slack UI



 PyTorch

Image  
Preprocessing



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Gemmini C binary  
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 FireSim

Run FireSim  
Rocket + Gemmini  
simulation

"It's a bridge"





# On-premise FPGA support now available!

- High-level of automation/reproducibility enabled by FireSim on AWS F1 cloud now extended to local/on-prem FPGAs:
  - Went from new machine with no FPGA attached to working FPGA-accelerated simulation in 1 hour and 40 mins
- Use existing FireSim features on both!
  - Debugging
    - Integrated Logic Analyzers, Trace dumps, Synth. assert/prints, co-simulation
  - Scale-to-AWS
  - Software support
    - FireMarshal workload management